**Xedge E1-2C/4CS LIMs**

**E1 LIMs for ATM, Frame Relay & Circuit Emulation Applications**

**INTRODUCTION**

The operational capabilities of an Xedge switch is determined in part by the slot controller in use and the number and type of associated line interface modules (LIMs). The Xedge E1 LIM is used for ATM cell switching, frame relay, channelized frame relay, and N x 64 Kbps Circuit Emulation over two or four physical ports.

Two models of E1 LIMs are available for use with their corresponding Xedge slot controller(s) in the Xedge switch:

- The Xedge E1-2CS dual port LIM is used with the Xedge PCX, PCE, CE, CHFRC, FRC, VSM, or ACP.
- The Xedge E1-4CS quad port LIM is used with the Xedge CE, CHFRC, or VSM.

**Specifications**

All physical and operational specifications apply to both the E1-2CS LIMs.

- **Standards:**
  - ITU-T G.703, G.804, I.432, ATM Forum UNI 3.0/3.1
  - Interface: E1
  - Connector Type: DB15: 120 ohms bal or 75 ohms unbal
  - Line Encoding: HDB3
  - Framing: G.704 Multiframework or unframed
  - Transmit Line: Automatic, 0 dB to 13.2 dB in 4 increments
  - Transmit Timing: From received clock; internal oscillator; primary or secondary system reference (line of Node Timing Module)

**LIM FEATURES**

- Dual or Quad port E1 2.048 Mbps interface or ATM, frame relay and circuit emulation applications.
- Comprehensive alarm reporting and performance monitoring
- Meets international ITU-T transmission standards

**Diagnostics & Alarms**

**Loopbacks**

The E1 LIMs support Transmit, Receive, and Payload Loopbacks

**Status Indications**

- IS (In Service)
- LS (Loss of Signal)
- AL (Alarm Loopback or Loss of Frame)

**Alarms & Performance**

The E1 LIMs support the physical layer alarms and performance monitoring statistics listed below:

- Loss Of Signal
- Loss Of Frame
- Alarm Indication Signal
- Far End Receiver Failure
- Line Coding Violations
- Line Errored Seconds
- Line Severely Errored Second
- Errored Seconds
- Severely Errored Seconds
- Alarm Indication Signal Seconds
- Unavailable Seconds
- Far End Errored Seconds

---

<table>
<thead>
<tr>
<th>Application</th>
<th>Controller</th>
<th>LIM</th>
<th>Ports</th>
</tr>
</thead>
<tbody>
<tr>
<td>ATM Cell Switching</td>
<td>ACP or PCX</td>
<td>E1-2SC/4CS</td>
<td>Up to 4</td>
</tr>
<tr>
<td>Frame Relay</td>
<td>FRC (Frame to ATM)</td>
<td>E1-2SC</td>
<td>Up to 2</td>
</tr>
<tr>
<td>Channelized Frame Relay</td>
<td>CHFRC (Ch Frame to ATM)</td>
<td>E1-2SC/4CS</td>
<td>Up to 4</td>
</tr>
<tr>
<td>T1 Circuit Emulation</td>
<td>CE or PCE</td>
<td>E1-2SC/4CS</td>
<td>Up to 4</td>
</tr>
<tr>
<td>Nx64 Kbps CE Emulation</td>
<td>VSM</td>
<td>E1-2SC/4CS</td>
<td>Up to 4</td>
</tr>
<tr>
<td>Voice over ATM</td>
<td>VSM</td>
<td>E1-2SC/4CS</td>
<td>Up to 4</td>
</tr>
</tbody>
</table>

All specifications subject to change without notice. © 2017 General Datacomm. All rights reserved. ® General Datacomm, GDC and the GDC logo are registered trademarks of General Datacomm, LLC.